Short note about FANIO usage

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FANIO is a 6U VME board. It accepts the differential ECL signals, and fan out to the CAEN TDCs (ECL level too). The 8-bit switch is used to select the input source. The inputs are 100 Ohm terminated. The switch setting:

Bit 1	Top RESET source selection	Left: PIN3+/PIN4-; Right: P2 backplane
Bit 2	Top CLOCK source selection	Left: PIN5+/PIN6-; Right: P2 backplane
Bit 3	Top TRIGGER source selection	Left: PIN1+/PIN2-; Right: P2 backplane
Bit 4	Bottom RESET source selection	Left: PIN9+/PIN10-; Right: P2 backplane
Bit 5	Bottom CLOCK source selection	Left: PIN7+/PIN8-; Right: P2 backplane
Bit 6	Bottom TRIGGER source selection	Left: PIN11+/PIN12-; Right: P2 backplane
Bit 7	Not used	
Bit 8	Not used	

*If you hold the FANIO upright, the switch Bit1 is the top one. The Right is the position near the VME connector.

The P2 backplane inputs are DC coupled. The PIN9+/PIN10-/PIN11+/PIN12- inputs are DC coupled, and the PIN1+/PIN2-/PIN3+/PIN4-/PIN5+/PIN6-/PIN7+/PIN8- inputs are AC coupled.

When the fanout cables are NOT connected to the CAEN TDCs, they need be terminated with the terminators (16-pin connectors with terminating resisters).