RACK: C2-6
TS OUTPUT

1A0
1
2
3
4
5
6
7
8
2A
3A
CLR
2S
3S
EN1
LV1
BUSY
OR1
GO
NC

ALL OUTPUTS ARE
SINGLE ENDED ECL
USE COAXIAL CONNECTION

TO
FORWARD
CARRIAGE
TRIGGER DISTRIBUTION

RACK: C2-6
Patch Panel

RG8 SOUTH CLAMSHELL
RG8 SPACE FRAME DECK 1
RG8 SPACE FRAME DECK 2
RG8 SPACE FRAME DECK 3

RACK: C2-6
VXI SLOT 12

seECL NIM
seECL NIM
seECL NIM
seECL NIM
seECL NIM
seECL NIM

EN1
BUSY
GO

VXI SLOT 10 [P3]
PIPELINE CLOCK BOARD
CLOCK
$\overline{\text{CLOCK}}$

VXI SLOT 0 [P3]
RACK: C2-6
TRIGGER INTERFACE BOARD
ENABLE
RUN/STOP

RACK: C2-6
PS-757 NIM 1
EN1
RACK: C2-6
VXI SLOT 12
NIM seECL

RACK: C2-6
PS-757 NIM 2
GO
VXI SLOT 12
NIM seECL

RACK: C2-6
PS-757 NIM 3
$\overline{\text{EN1}}$
$\overline{\text{BUSY}}$
RACK: C2-6
PS-757 NIM 4
"LIVE_GATE"
EN1 * BUSY
SCALER #3
VETO

RACK: C2-6
PS-757 NIM 5
GO
$\overline{\text{EN1}}$
"RUNGATE"
$\overline{\text{GO}}$ * EN1
SCALER #2
VETO

Clock Module
10KHZ CLOCK

FARADAY CUP
Clock From Deck 3

CLOCK AND FARADAY CUP SIGNAL FANOUT
SIGNALS GO TO NIM->ECL CONVERTERS
SIGNALS ARE DISTRIBUTED FROM NIM ->ECL CONVERTERS

CAEN V560 VME SCALERS
MODULES UPGRADED JAN00

SCALER 1 xFF0300
10khz Clock
Faraday Cup
1Mhz Clock
unused
MOR.ST
MOR.PC
MOR.PS
MOR.TAC
Master OR
Pair Counter
Pair Spectrometer
Total Absorbtion Cntr
ST [STart Counter]
unused
10Khz Clock
Faraday Cup
NO VETO

SCALER 2 xFF0400
10khz Clock
Faraday Cup
unused
unused
MOR.ST
MOR.PC
MOR.PS
MOR.TAC
Master OR
Pair Counter
Pair Spectrometer
Total Absorbtion Cntr
ST [STart Counter]
unused
10Khz Clock
Faraday Cup
RUNGATED

SCALER 3 xFF0500
10khz Clock
Faraday Cup
1Mhz Clock
unused
unused
unused
unused
unused
Master OR
unused
unused
unused
unused
unused
10Khz Clock
Faraday Cup
LIVEGATED

SCALER 4 xFF0600
TS prescale 1
TS prescale 2
TS prescale 3
TS prescale 4
TS prescale 5
TS prescale 6
TS prescale 7
TS prescale 8
TS prescale 9
TS prescale 10
TS prescale 11
TS prescale 12
TS OR Output
TS Level 1 accept
unused
unused
NO VETO

SCALER 5 xFF0700
Level 2 FAIL
Level 2 PASS
Level 2 START
Level 2 CLEAR
Level 2 ACCEPT
Level 3 ACCEPT
unused
unused
unused
unused
unused
unused
unused
unused
unused
unused
unused
NO VETO

TRIGGER SUPERVISOR OUTPUT
SIGNAL DISTRIBUTION CHART
Scaler 'Vetoes' & Pipeline Clock Control

Updated 26 Jan00