CLAS Level 2 Trigger System

- Overall Trigger Block Diagram
- Level 2 VME Majority Logic Block Diagram
  Majority Logic Schematic
- Final Latch of Level 1 & Level 2 signals

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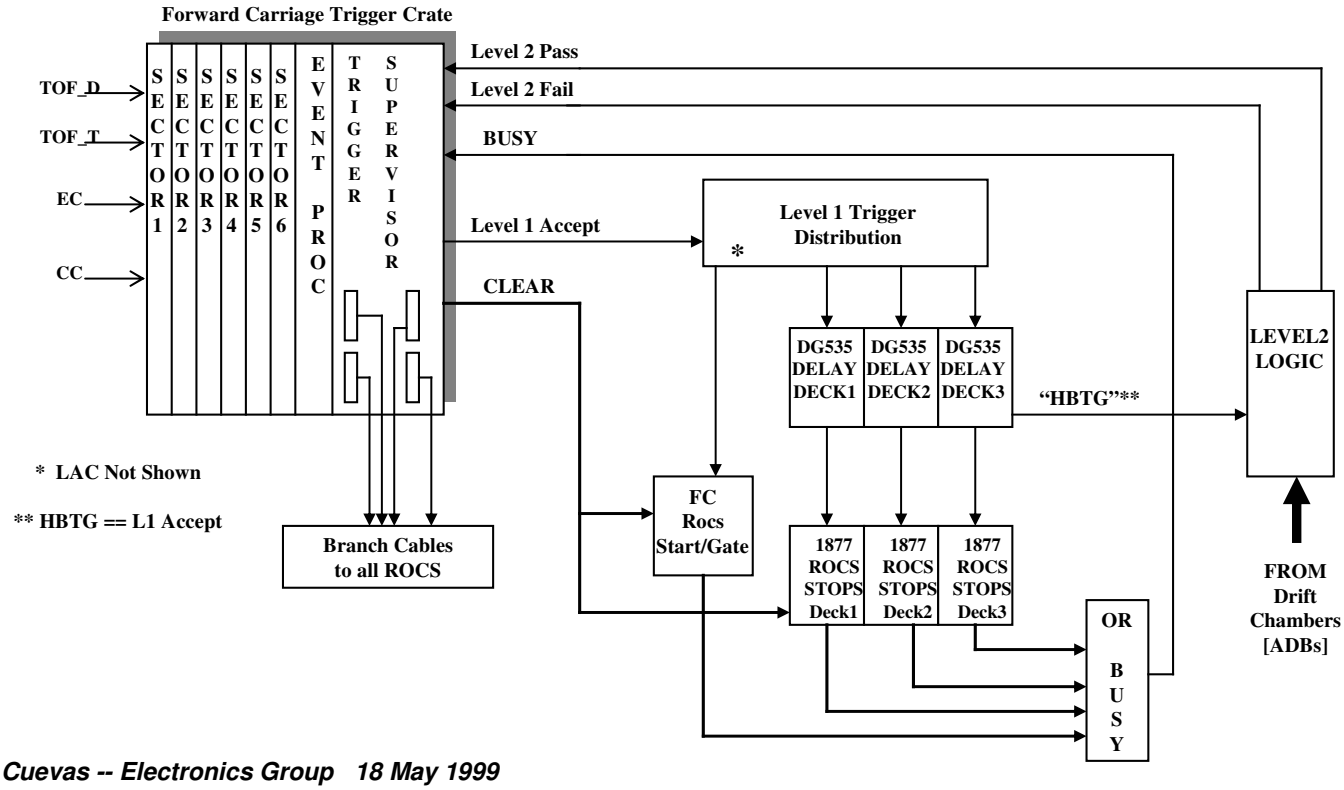
Overall Trigger Block Diagram



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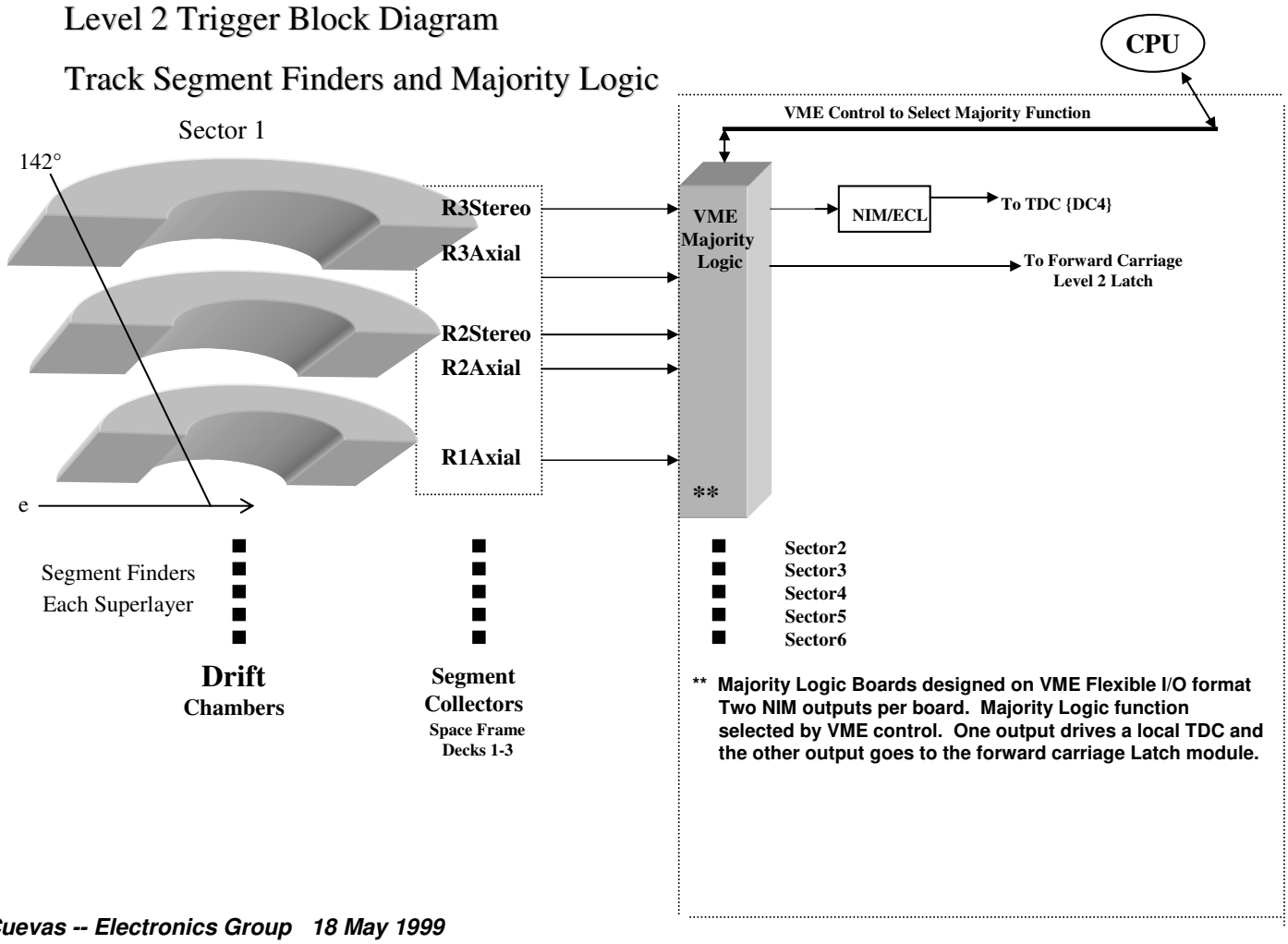
Level 2 Trigger Block Diagram

Track Segment Finders and Majority Logic



** Majority Logic Boards designed on VME Flexible I/O format Two NIM outputs per board. Majority Logic function selected by VME control. One output drives a local TDC and the other output goes to the forward carriage Latch module.

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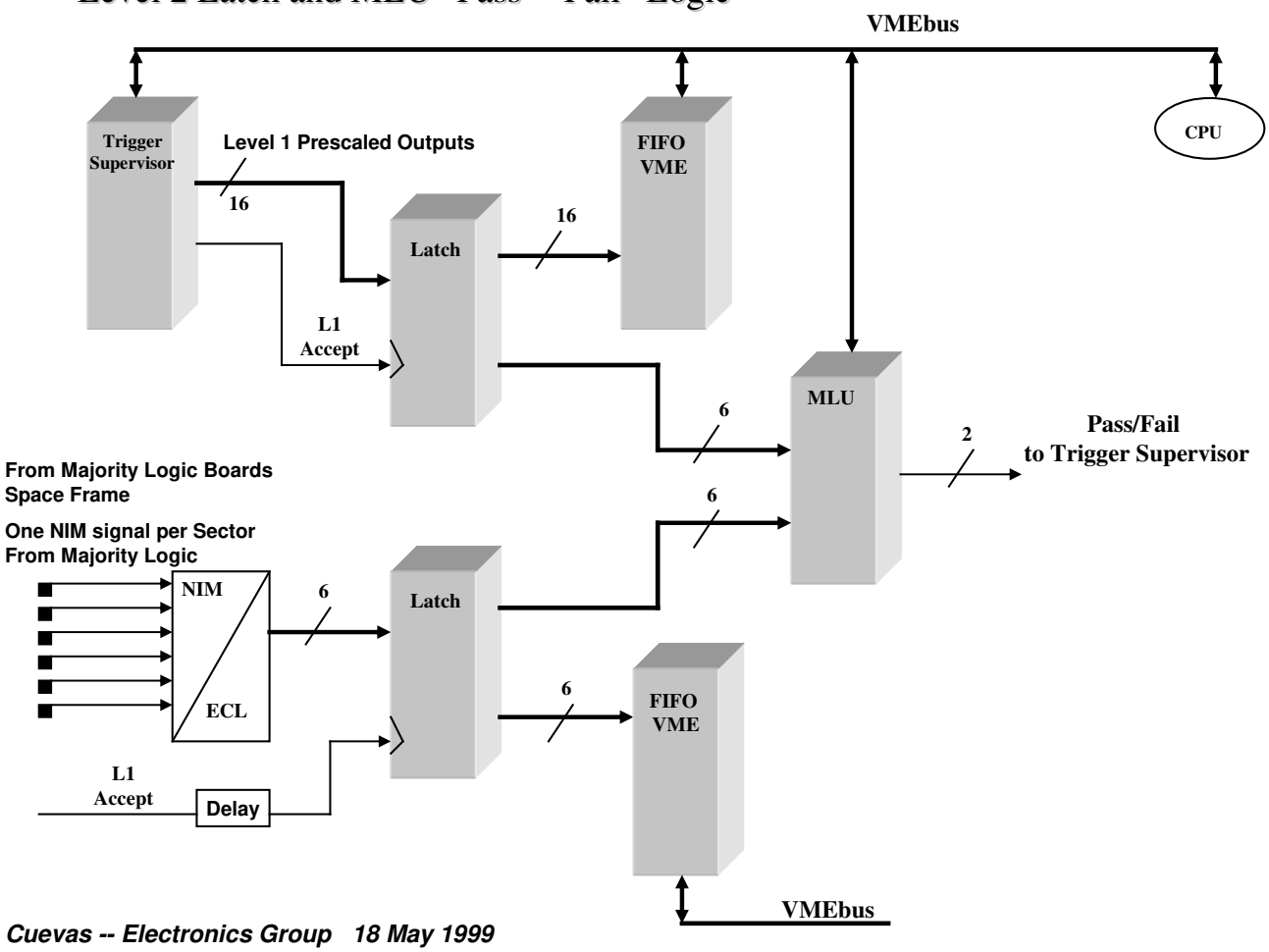
Level 2 Latch and MLU "Pass -- Fail" Logic



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