VXI "D" CRATE
TRIGGER SUPERVISOR
RacK: C2-6
OUTPUT **
PORT

1A0
1
2
3
4
5
6
7
8
2A
3A
CLR
2S
3S
EN1
LV1
BUSY
OR1
GO
NC

L1

Coax

RACK: C2-6
NIM BIN

SHEET 7
MIN INSERTION DELAY

J1 [ADC GATE] O1

Delay OUT 1

200nS
Cable

Delay IN 1

Delay OUT 2

DELAY IN 2

[TDC START] O2

200nS

Coax

SHEET 9
FANOUT
sECL TO diffECL

P1

P2
P3
P4
P5
P6 Spare
P7 Spare
RACK: C2-6 P8 Spare
NIM BIN P9

Twinaxial
Cables

FORWARD CARRIAGE DECK 2
[ADC Gates]
Fastbus Signal Distribution Boards

J10 FastBus Crate EC1;Rack C2-1;
J10 FastBus Crate EC2;Rack C2-4;
J10 FastBus Crate CC1 & Tag;Rack C2-5;
J10 FastBus Crate TOF;Rack C2-8;

4.0uS

Coax

SHEET 8
FANOUT
sECL TO sECL

P1

P2
P3
P4
P5
P6
RACK: C2-6 P7 Spare
NIM BIN P8 Spare

Coax

FORWARD CARRIAGE DECK 2
[TDC Start]
Fastbus Signal Distribution Boards

J5 FastBus Crate EC1;Rack C2-1;
J5 FastBus Crate EC2;Rack C2-4;
J5 FastBus Crate CC1 & Tag;Rack C2-5;
J5 FastBus Crate TOF;Rack C2-8;
TOF Calibration Pulser → To Discriminator Test Inputs

794

Delay

Trig

seECL NIM

NIM seECL

4.0uS

RACK: C2-6
NIM BIN

Trigger Signal Distribution
to
Forward Carriage Readout Crates
ADC Gates & TDC Starts { MPI }

** Outputs are Single Ended ECL signals. Use coaxial connection.

CEBAF
Continuous Electron Beam Accelerator Facility
United States Department of Energy
Newport News, Virginia

Fast Electronics Group

Title: Hall B Trigger Fanout Block Diagram

Engr. CUEVAS		
Dfts.		
Appr.	Size B	Number / Rev
Date 1/97	Filename SHEET A	Sheet of