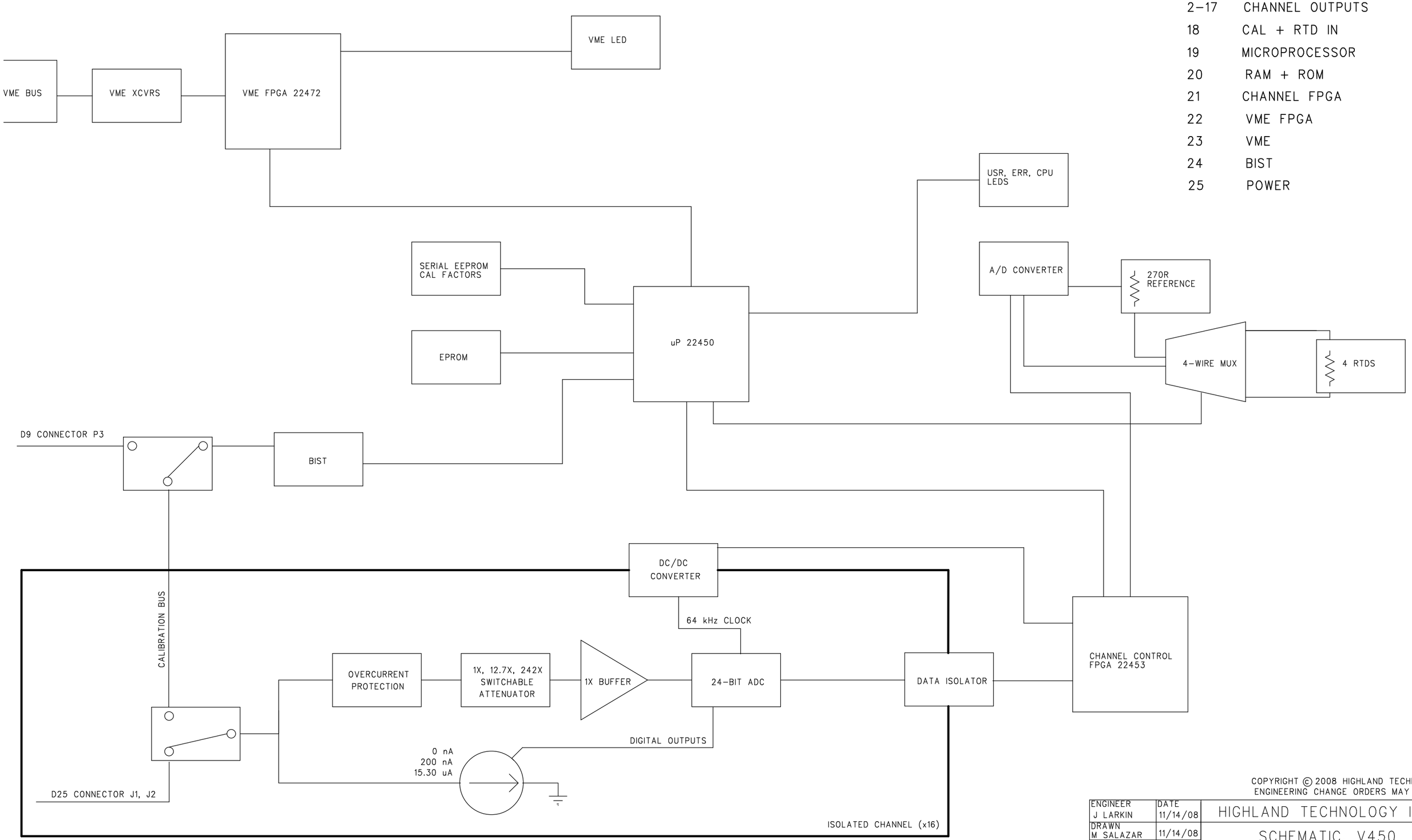
1	BLOCK DIAGRAM
2–17	CHANNEL OUTPUTS
18	CAL + RTD IN
19	MICROPROCESSOR
20	RAM + ROM
21	CHANNEL FPGA
22	VME FPGA
23	VME
24	BIST
25	POWER



COPYRIGHT © 2008 HIGHLAND TECHNOLOGY
ENGINEERING CHANGE ORDERS MAY APPLY

ENGINEER J LARKIN	DATE 11/14/08	HIGHLAND TECHNOLOGY INC.
DRAWN M SALAZAR	11/14/08	SCHEMATIC, V450 16-CHANNEL ANALOG IN
CHECKED		
APPROVED		
RELEASED		DRAWING NO: 22S450 REV: E
		SHEET: 1 OF 25 FILENAME:22S450E.SCH