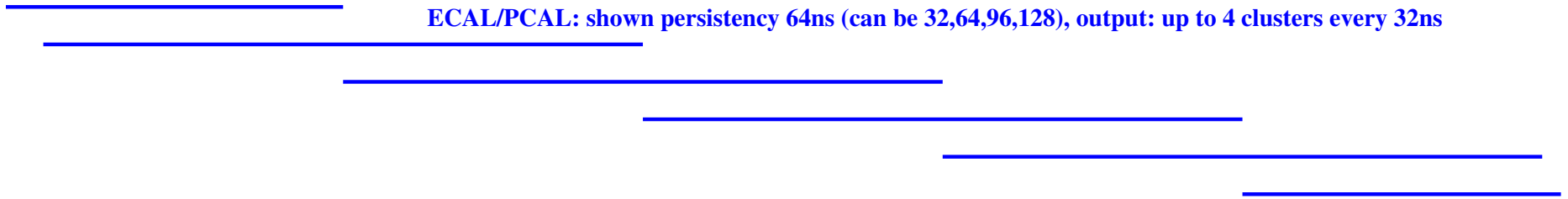


Trigger timing (stage 1)

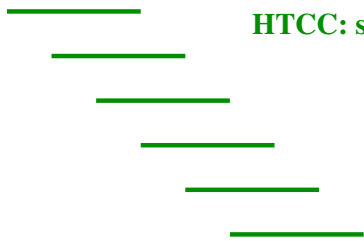
FADC: input every 32ns: 13bit energy, 3bit time



ECAL/PCAL: shown persistency 64ns (can be 32,64,96,128), output: up to 4 clusters every 32ns



HTCC: shown persistency 12ns (can be 4,8,12,16,20,24,28,32), output: 36-bit cluster mask every 4ns



FTOF1B LEFT

FTOF1B: shown L-R coincidence window 32ns (can be from 0 to 32), output: 62-bit mask every 4ns at average time

FTOF1B RIGHT

